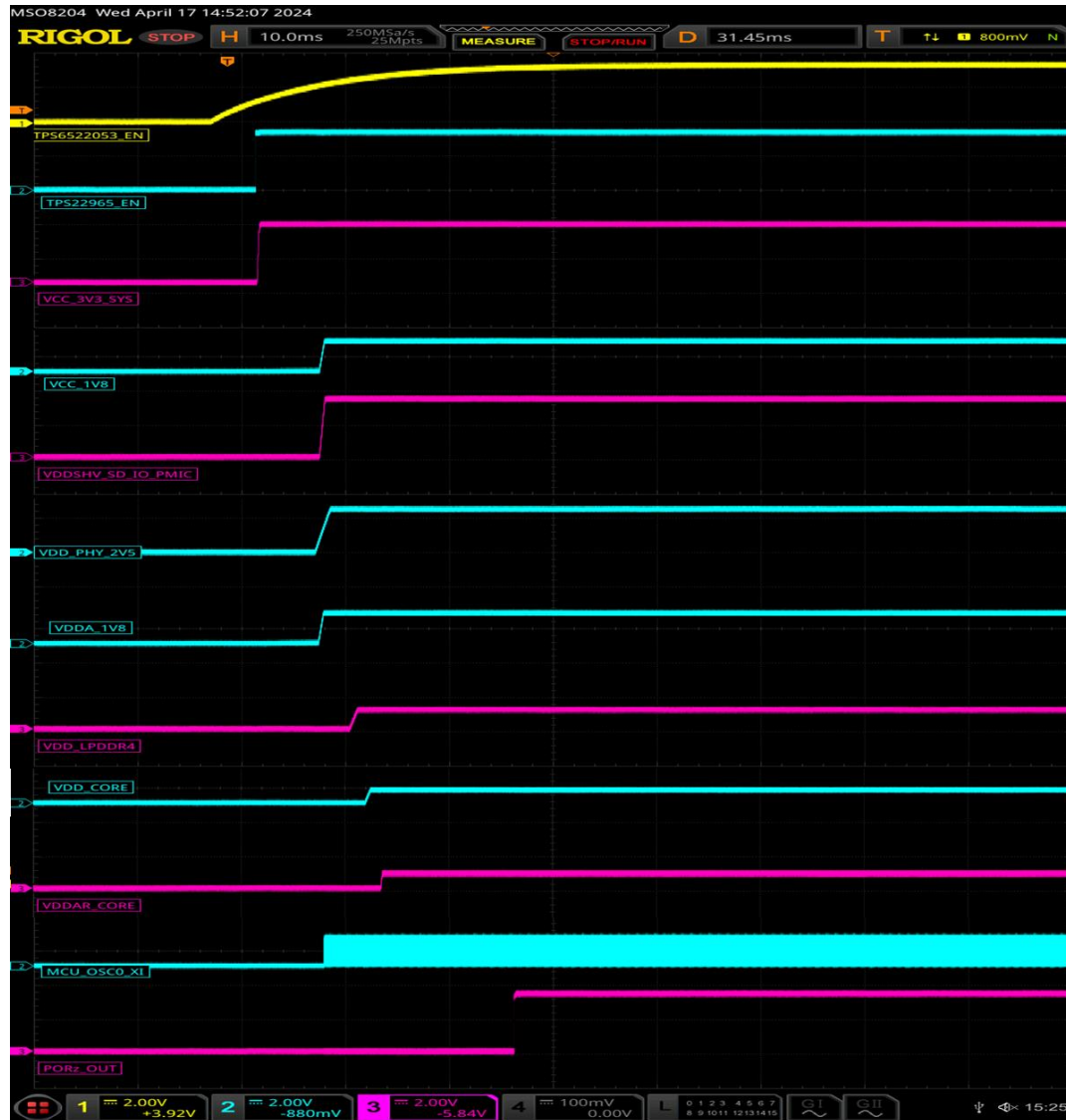


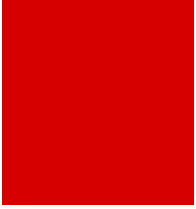
SK-AM64B
PROC101A
Power up Sequence

Power Up sequence Timing Measurements

Power Up Sequence		
Measurements are Done w.r.t TPS6522053_EN		
Sl.No	Supply Rail	Time delay w.r.t TPS6522053_EN
1	TPS6522053_EN	0ms
2	TPS22965_EN	4ms
3	VCC_3V3_SYS	4.15ms
4	VCC1V8	10.3ms
5	VDDSHV_SD_IO_PMIC	10.5ms
6	VDD_PHY_2V5	10.6ms
7	VDDA_1V8	10.4ms
8	VDD_LPDDR4	13.4ms
9	VDD_CORE	14.9ms
10	VDDAR_CORE	16.1ms
11	MCU_OSCO_XI	10.6ms
12	PORz_OUT	29ms



Power Up Sequence Timing Measurements

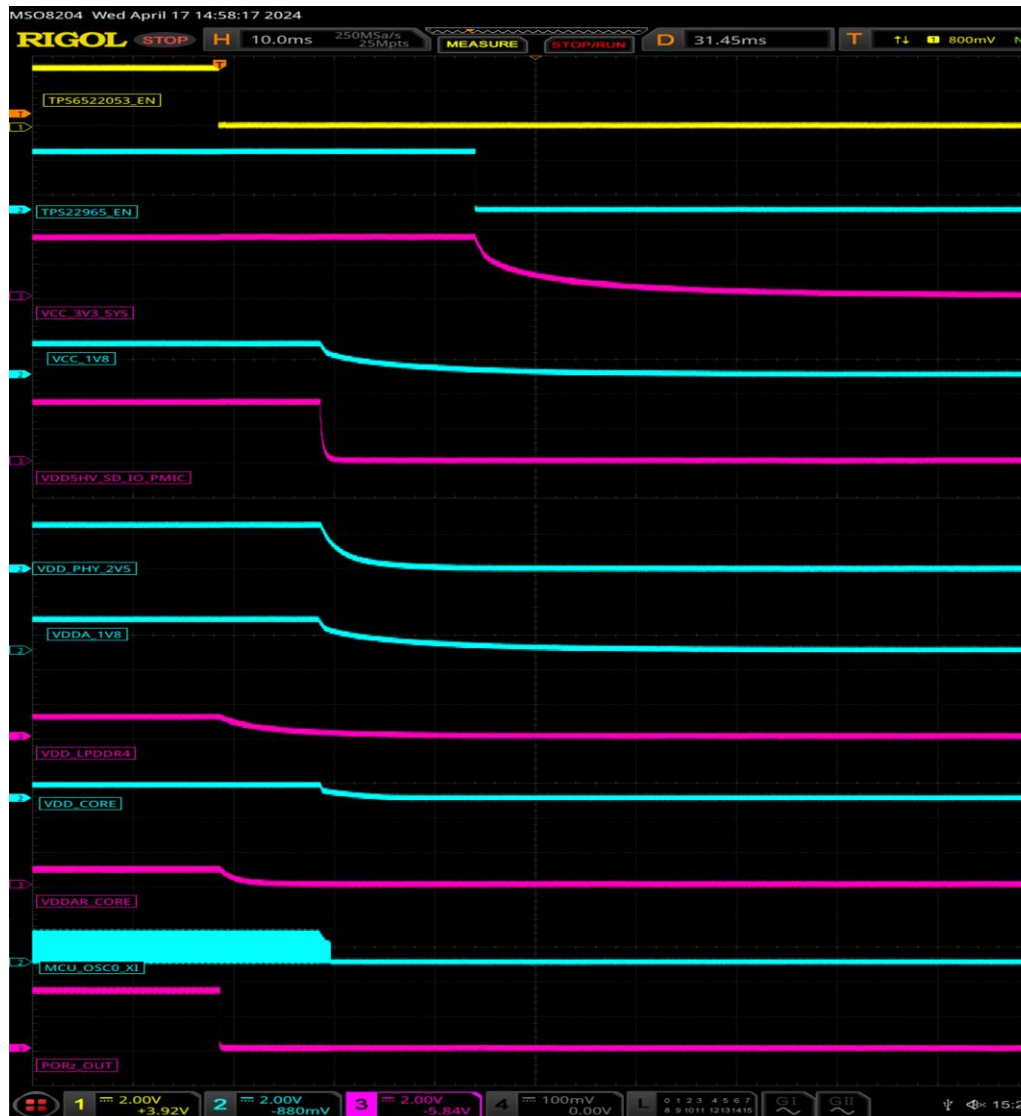


SK-AM64B PROC101A

Power Down Sequence

Power Down Sequence Timing Measurements

Power Down Sequence		
Measurements are Done w.r.t TPS6522053_EN		
Sl.No	Supply Rail	Time delay w.r.t TPS6522053_EN
1	TPS6522053_EN	0ms
2	TPS22965_EN	25.4ms
3	VCC_3V3_SYS	25.4ms
4	VCC1V8	10.3ms
5	VDDSHV_SD_IO_PMIC	10.3ms
6	VDD_PHY_2V5	10.5ms
7	VDDA_1V8	9.8ms
8	VDD_LPDDR4	10.8ms
9	VDD_CORE	10.3ms
10	VDDAR_CORE	1ms
11	MCU_OSC0_XI	11.3ms
12	PORz_OUT	500us



Power Down Sequence Timing Measurements

Combined

